In The Claims

Please amend Claims 1 and 3 as follows:

Claim 1 (currently amended)

1.   A transconductance power amplifier for amplifying a signal to a capacitive load, comprising:

only two N-channel MOSFET transistors, including;

a first N-channel enhancement MOSFET transistor operatively arranged to source current to said capacitive load, wherein said first N-channel MOSFET transistor has a threshold gate to source voltage;

a second N-channel enhancement MOSFET transistor operatively arranged to sink current to said capacitive load, said amplifier further comprising;

an operational amplifier ~~operatively~~ arranged to transmit and amplify an input signal to both of said first and second MOSFET transistors; and,

means for biasing said first N-channel enhancement MOSFET transistor such that its gate to source voltage is always at or above its threshold when the load draws near zero current so that very little additional gate charge is required to turn it on more fully.

Claim 2 (original)

2.   The transconductance power amplifier for amplifying a signal to a capacitive load recited in Claim 1 further comprising means for reducing current to said first N-channel enhancement MOSFET transistor when said power amplifier sinks current from the load through said second N-channel enhancement MOSFET transistor.

Claim 3 (currently amended)

3.   A transconductance power amplifier for amplifying a signal to a capacitive load, comprising:

only two N-channel MOSFET transistors, including;

a first N-channel enhancement MOSFET transistor operatively arranged to source current to said capacitive load;

a second N-channel enhancement MOSFET transistor operatively arranged to sink current to said capacitive load, said amplifier further comprising;

an operational amplifier ~~operatively~~ arranged to transmit and amplify an input signal to both of said first and second MOSFET transistors; and,

means for reducing current to said first N-channel enhancement MOSFET transistor when said power amplifier sinks current from the load through said second N-channel enhancement MOSFET transistor.

Please add new Claims 4 and 5 as follows:

Claim 4 (new)

4.   A transconductance power amplifier for amplifying a signal to a capacitive load, comprising:

a maximum of five (5) active components, including;

a first N-channel enhancement MOSFET transistor operatively arranged to source current to said capacitive load, wherein said first N-channel MOSFET transistor has a threshold gate to source voltage;

a second N-channel enhancement MOSFET transistor operatively arranged to sink current to said capacitive load, said amplifier further comprising;

an operational amplifier arranged to transmit and amplify an input signal to both of said first and second MOSFET transistors; and,

means for biasing said first N-channel enhancement MOSFET transistor such that its gate to source voltage is always at or above its threshold when the load draws near zero current so that very little additional gate charge is required to turn it on more fully.

Claim 5 (new)

5.   The transconductance power amplifier recited in Claim 4 wherein said power amplifer comprises a single operational amplifier, only two MOSFET transistors, and only two bipolar transistors.